

PINNING

SYMBOL	PIN	DESCRIPTION
OUTA	1	output channel A
n.c.	2	not connected
n.c.	3	not connected
n.c.	4	not connected
n.c.	5	not connected
n.c.	6	not connected
EQA	7	equalizing output channel A
EQFA	8	equalizing input channel A
V <sub>CC</sub>	9	voltage supply
INA1	10	input channel A1 (forward or reverse)
V <sub>REF</sub>	11	reference voltage
INA2	12	input channel A2 (reverse or forward)
INB2	13	input channel B2 (reverse or forward)
HS	14	headswitch input
INB1	15	input channel B1 (forward or reverse)
GND	16	ground
EQFB	17	equalizing input channel B
EQB	18	equalizing output channel B
EQS	19	equalizing switch input
n.c.	20	not connected
ACUR	21	auxiliary current
n.c.	22	not connected
n.c.	23	not connected
OUTB	24	output channel B

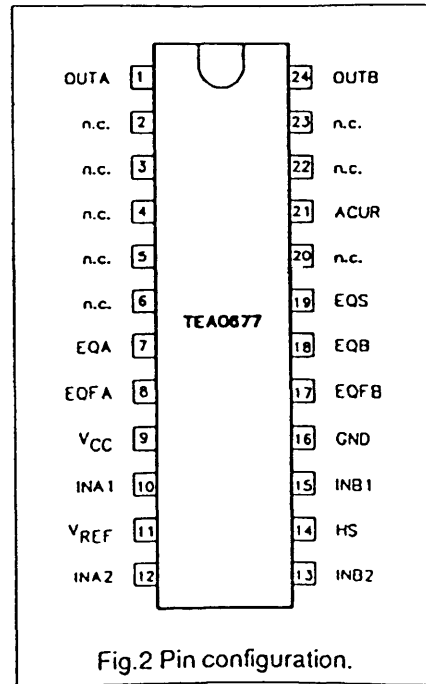
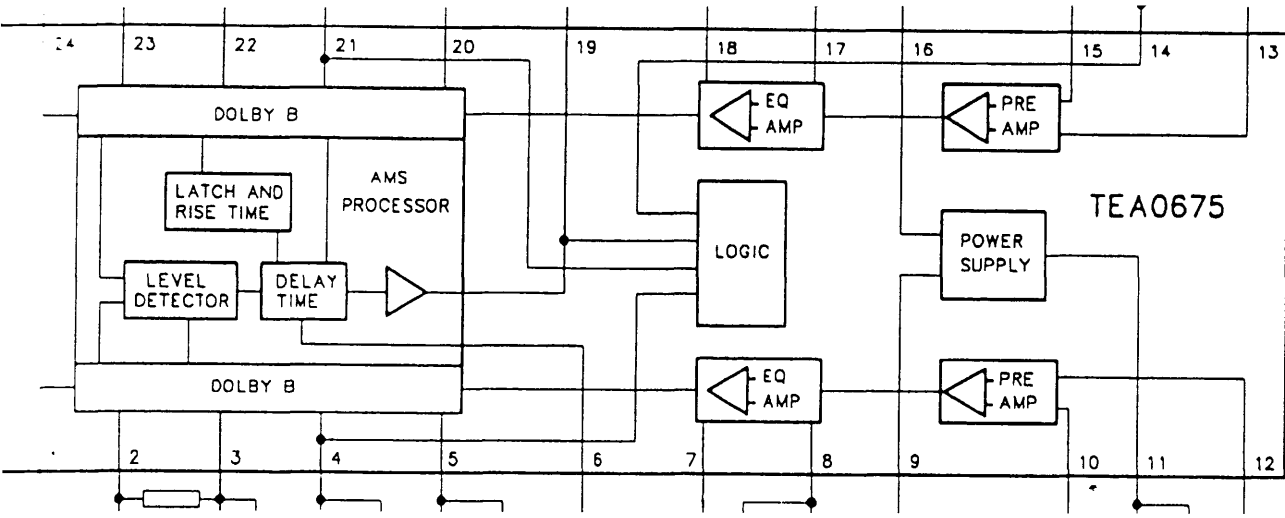


Fig.2 Pin configuration.

PINNING

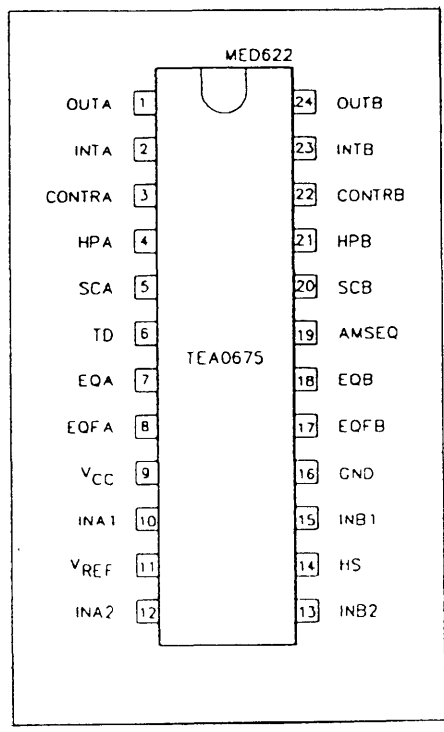
SYMBOL	PIN	DESCRIPTION
OUTA	1	output channel A
INTA	2	not connected
CONTRA	3	not connected
HPA	4	not connected
SCA	5	not connected
TD	6	not connected
EQA	7	equalizing output channel A
EQFA	8	equalizing input channel A
V <sub>CC</sub>	9	voltage supply
INA1	10	input channel A1 (forward or reverse)
V <sub>REF</sub>	11	reference voltage
INA2	12	input channel A2 (reverse or forward)
INB2	13	input channel B2 (reverse or forward)
HS	14	headswitch input
INB1	15	input channel B1 (forward or reverse)
GND	16	ground
EQFB	17	equalizing input channel B
EQB	18	equalizing output channel B
AMSEQ	19	equalizing switch input
SCB	20	not connected
HPB	21	auxiliary current
CONTRB	22	not connected
INTB	23	not connected
OUTB	24	output channel B

11 TEA0675

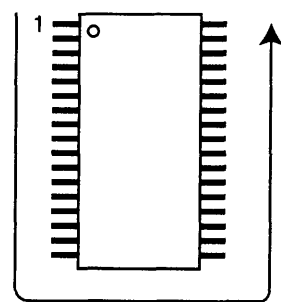
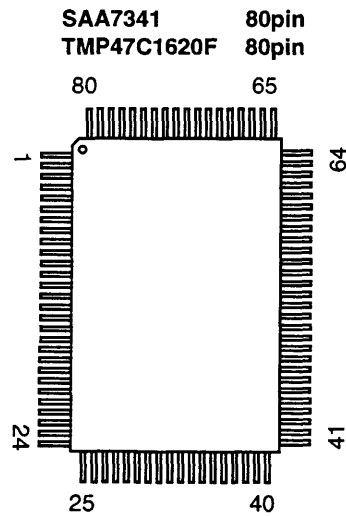
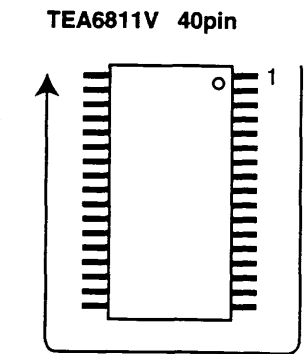
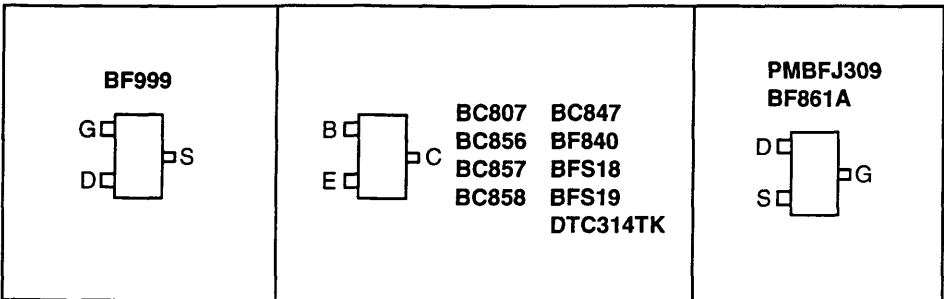
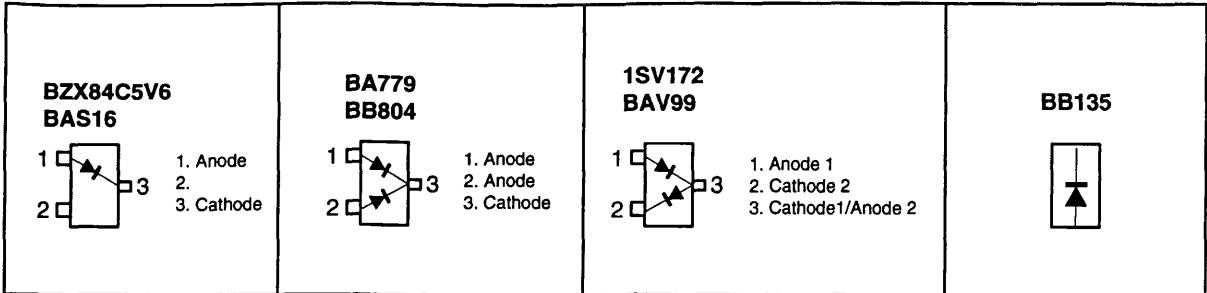
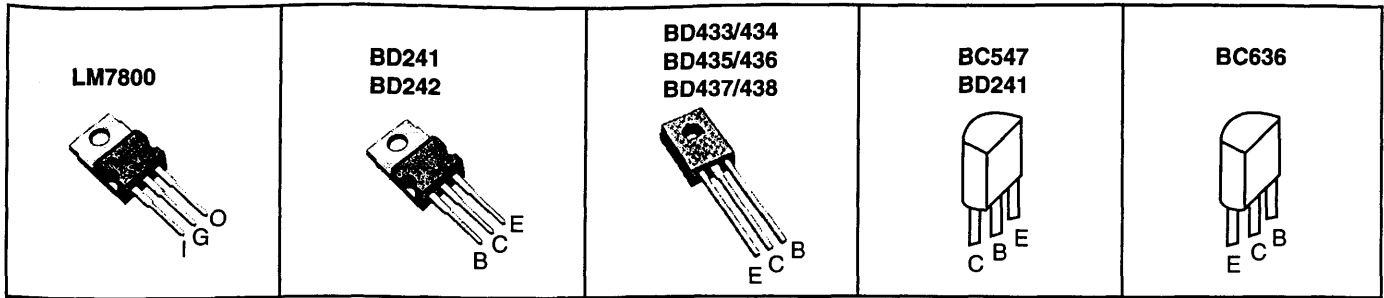


NING

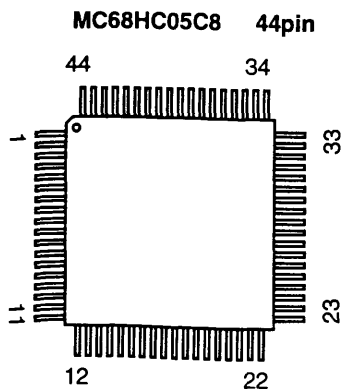
SYMBOL	PIN	DESCRIPTION
OUTA	1	output channel A
INTA	2	integrating filter channel A
CONTRA	3	control voltage channel A
HPA	4	high-pass filter channel A
SCA	5	side chain channel A
	6	delay time constant
EQFA	7	equalizing output channel A
EQFA	8	equalizing input channel A
VCC	9	voltage supply
IN1	10	input channel A1 (forward or reverse)
VREF	11	reference voltage
IN2	12	input channel A2 (reverse or forward)
	13	input channel B2 (reverse or forward)
	14	headswitch input
IN1	15	input channel B1 (forward or reverse)
ND	16	ground
EQFB	17	equalizing input channel B
EQFB	18	equalizing output channel B
AMSEQ	19	AMS output and EQ-switch input
SCB	20	side chain channel B
HPB	21	high-pass filter channel B
CONTRB	22	control voltage channel B
INTB	23	integrating filter channel B
OUTB	24	output channel B



# TERMINAL GUIDE OF IC'S, TRANSISTORS AND DIODES



- 24W04C6** 8pin
- 35098A** 8pin
- L2722** 8pin
- LM258** 8pin
- TCA0372DPI** 8pin
- MC45581DT** 8pin
- TC35098AP** 8pin
- TDA8579T/N1** 8pin
- HEF4013BT** 14pin
- HEF4053BT** 16pin
- SAA6579T** 16pin
- HEF4052BT** 16pin
- TEA6330T/V1** 20pin
- TDA8808T/C3** 28pin
- TDA8809/C2** 28pin
- MSM5165AL** 28pin
- TEA6320** 32pin
- MSM6307GS** 32pin
- TEA6822T** 56pin



# 7602 TDA 7374

## TDA7374

### PIN CONNECTION (Top view)

3 3 6100 B 5  
 3 3 6102 B 5  
 3 3 6200 B 5  
 A 2 6201 A 5  
 \ 3 6202 A 5  
 \ 2 7100 A 5  
 \ 5 7200 A 4  
 \ 5 7201 A 5  
 \ 5 7202 A 4  
 A 5 7300 A 3  
 A 5 7301 A 2

